

PTO/SB/08a (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1

of 4

Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5742840	04-21-1998	Hansen et al.	
		US-5748872	05-05-1998	Norman	
		US-5860080	01-12-1999	James et al.	
		US-5867422	02-02-1999	John	
		US-5898863	04-27-1999	Ofer et al.	
		US-6006318	12-21-1999	Hansen et al.	
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		US-6092229	07-18-2000	Boyle et al.	
		US-6125419	09-26-2000	Umemura et al.	
		US-6151648	11-21-2000	Haq	
		US-6154826	11-28-2000	Wulf et al.	
		US-6160423	12-12-2000	Haq	
		US-6255859	07-03-2001	Haq	
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		US-6408402	06-18-2002	Norman	
		US-6487102	11-26-2002	Halbert et al.	

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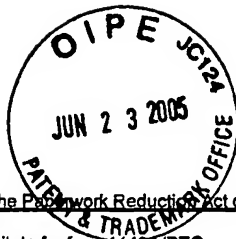
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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Sheet **2**of **4****Complete if Known**

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		Number-Kind Code ² (if known)			
		US-6502161	12-31-2002	Perego et al.	
		US-6513080	01-28-2003	Haq	
		US-6154855	11-28-2000	Norman	
		US-6345321	02-05-2002	Litaize et al.	
		US-6369605	04-09-2002	Bonella et al.	
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Sheet

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of

4

Complete if Known

Application Number

10/699,116

Filing Date

October 31, 2003

First Named Inventor

Donnelly

Art Unit

2187

Examiner Name

Chace, C.

Attorney Docket Number

RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt	
		*U.S. Patent Application Serial No. 10/859,060, filed 05-31-2004 by Pete D. Vogt	
		NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39	
		SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42	

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*Please note that the copies of these references enclosed herewith were obtained from the file history for U.S. Patent Application Serial No. 10/456,353. Applicants' attorney assumes that they are the correct copies.

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				<i>Application Number</i>	10/699,116
				<i>Filing Date</i>	October 31, 2003
				<i>First Named Inventor</i>	Donnelly
				<i>Art Unit</i>	2187
				<i>Examiner Name</i>	Chace, C.
				<i>Attorney Docket Number</i>	RAMB-01065US1
Sheet	4	of	4		

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U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-6016282	1-18-2000	Keeth	
		US-6449727	09-10-2002	Toda	
		US-4644532	02-17-1987	George et al.	
		US-4667305	05-19-1987	Dill et al.	
		US-4747100	05-24-1988	Roach et al.	
		US-4864563	09-05-1989	Pavey et al.	
		US-5228134	07-13-1993	MacWilliams et al.	
		US-5355467	10-11-1994	MacWilliams et al.	
		US-5408646	04-18-1995	Olnowich et al.	
		US-5537394	07-16-1996	Abe et al.	
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		US-5787083	07-28-1998	Iwamoto et al.	
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 99/30240	06-17-1999	Intel Corporation		
		WO 99/41666	08-19-1999	Intel Corporation		
		WO 99/41667	08-19-1999	Intel Corporation		
		EP 1069509 A2	01-17-2001	Texas Instruments Incorporated		

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		US-5802565	09-01-1998	McBride	
		US-5805798	09-08-1998	Kearns et al.	
		US-5848145	12-08-1998	Gallagher et al.	
		US-5884036	03-16-1999	Haley	
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		US-6493250	12-10-2002	Halbert et al.	
		US-6530006	03-04-2003	Dodd et al.	
		US-6449679	09-10-2002	Ryan et al.	
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		US-6208273	03-27-2001	Dye et al.	
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		US-20040260991	12-23-2004	Vogt et al.	
		US-20040250181	12-09-2004	Vogt et al.	
		US-20040250153	12-09-2004	Vogt	
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		US-20040015650	01-22-2004	Zumkehr et al.	
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First Named Inventor

Donnelly

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2187

Examiner Name

Chace, C.

Attorney Docket Number

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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		KARABATSOS, C., "Quad Band Memory (QBM) Technology", Kentron Technologies, Inc., April 2001	
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		GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SLD RAM Consortium, SLD RAM Inc., pp. 1-14 (Aug. 1997)	
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		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004	

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